

## ABSTRACT OF THE DISCLOSURE

A semiconductor device includes external interface terminals and processing circuits, and it is fed with an operating power source when detachably set in a host equipment. Power source feeding terminals (VCC, VSS) among the external interface terminals are long enough to keep touching the corresponding terminals of the host equipment for, at least, a predetermined time period since the separation of an extraction detecting terminal among the external interface terminals, from the corresponding terminal of the host equipment, and they are formed to be longer in the extraction direction of the semiconductor device than the extraction detecting terminal. The processing circuits include an interface control circuit (2) which is connected to the external interface terminals, and a rewritable nonvolatile memory (3) which is controlled by the interface control circuit. The nonvolatile memory stores information on the basis of the difference of the threshold voltages of memory cells. The interface control circuit causes a storage area midway of a threshold voltage initialization process to complete a process in which the threshold voltages of the memory cells are uniformalized into a predetermined threshold voltage distribution, before the power source is cut off after the separation of the extraction detecting terminal from the corresponding terminal of the host equipment.